

# Core losses analysis of the LCL filter inductor for SiC-based inverter

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**Abstract.** The ability of SiC devices to switch at high speed allows increasing significantly the power density in both converters and passive components, reducing their required size. To mitigate harmonic injection from inverters into the grid, in order to comply with power quality standards, an accurate filter design is required. Given its excellent performance, an LCL filter is the configuration most suitable in grid-connected power converters. Several parameters must be considered when designing an effective LCL filter, and the inverter-side inductor assumes a special importance because of its relevance to suppress high frequency harmonic content at the inverter side. One of the most relevant issues to be considered in the process of designing the LCL filter is the evaluation of core losses in the inverter-side inductor, which will determine the final temperature of the inductor.

This paper analyses the core losses of the inverter-side inductor of an LCL filter. The proposed method is based on the computation of the current harmonics generated by the inverter and on Steinmetz's empirical equation. As a result, core losses calculated taking into account several carrier and sideband harmonics show good agreement with the experimental values. When current harmonics are estimated by simulation, as it is done in the proposed design procedure, results are less accurate, but precise enough for a design procedure.

**Key words.** Core losses, LCL filter, SiC inverter, power quality.

## 1. Introduction

Improving energy and power densities is a major challenge in power inverters. Transport industry (electric vehicle, aircraft, marine, ...) as well as domestic and industrial applications require power converters that must be both compact and lightweight. Hence, the achievement of high power density converters is necessary [1]-[2]. Power density can be improved by increasing the switching frequency of the converter [3]. Silicon carbide (SiC) is a semiconductor material suitable for efficient power conversion with higher operating frequency than conventional materials like silicon (Si) [4]-[6].

However, due to the switching frequencies in SiC-based inverters, current harmonics are injected into the grid. The harmonic content must be reduced to ensure compliance with power quality requirements of grid codes. To achieve this, an LCL filter is a common choice in power converters for its excellent performance [7]. Another aspect worth considering is that inductors contribute significantly to the overall weight and volume of converters. Inductors volume can be reduced depending on the maximum flux density that the magnetic material can support without saturating [8]. However, due to low permeability, moderate core loss properties and gradual saturation characteristics of certain core materials, designs can be more limited by temperature rise than by magnetic saturation [9]. Due to the great influence that core losses have on the increase in the temperature of the inverter, its analysis is crucial in order to design an inductor with the lowest possible core losses.

This paper analyses the accuracy in the estimation of core losses by using Steinmetz's equation based on the Fourier expansion of the inductor current, comparing them with the experimental measurements in the inverter-side inductor of the LCL filter in a 20-kW SiC inverter.

## 2. Design of the inverter-side inductor of the LCL filter

A Metglas (amorphous iron alloy) core has been selected for the side-inverter inductor (Fig. 1) due to its high power density. For the grid-side inductor, an iron core is preferred due to its high saturation flux density and low costs.

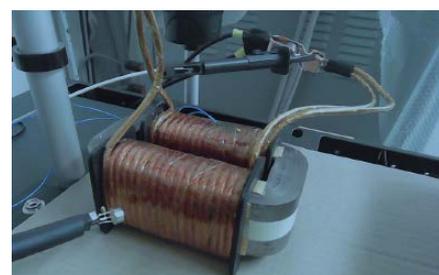


Fig. 1. The inverter-side inductor of the SiC inverter filter.

Fig. 2 shows the current harmonic content for the designed 20 kW, 25 kHz SiC inverter. Firstly, it can be noticed that baseband harmonics are low; secondly, higher harmonics are around the switching frequency and its multiples. Moreover, for a maximum current ripple of 20% on the inverter side, the inductance is inversely proportional to the frequency [10]-[11].

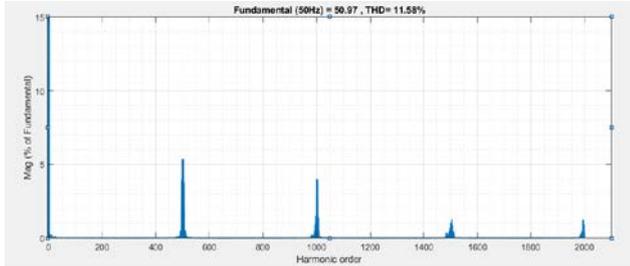


Fig. 2 Current harmonic content.

The losses generated in the winding  $P_w$  and in the core  $P_c$  increase the inductor temperature. The temperature rise  $\Delta T$  is expressed as

$$\Delta T = (P_w + P_c) \cdot R_{th} \quad (1)$$

where  $R_{th}$  is the total thermal resistance of the inductor.  $R_{th}$  includes [12]:

- the thermal resistance between the core and the ambient
- the thermal resistance between the conductor and the ambient
- the conduction thermal resistance of the conductor and the radiation thermal resistance between the conductor surface and the core.

The inverter-side inductor losses are one of the main contributions to the total filter losses; therefore, their precise evaluation is required in the filter design process.

### 3. Core losses analysis

#### A. Classical eddy current and hysteresis losses

To improve inductor size, efficiency and transient response, low inductance values (limited by the maximum current ripple) for the inverter-side are preferred. Core losses increase temperature [13], which is usually the main limitation in high-frequency applications.

The power loss density in the core of the inductor is due to both phenomena, hysteresis and eddy currents.

On the one hand, hysteresis losses are due to the magnetization and demagnetization of the core as alternating current flows, which is caused by the resistance of the molecules in the core to be moved. The instantaneous power delivered to an inductor over one full cycle  $p(t)$  is the multiplication of the voltage  $v_L(t)$  by the current  $i_L(t)$

$$p(t) = v_L(t) \cdot i_L(t) \quad (2)$$

The energy  $E_h$  supplied to the inductor during a period  $T$  is

$$E_h = \int_0^T i_L v_L dt = N \cdot S \oint i_L dB = S \cdot \ell \oint H dB \quad (3)$$

where  $B$  is the magnetic flux density,  $H$  the magnetic field intensity,  $N$  the number of turns in the coil,  $S$  the cross-sectional area and  $\ell$  the core length.

Therefore, the hysteresis power loss density is given by

$$P_h = \frac{E_h}{T \cdot V} = f \cdot \oint H dB \quad (4)$$

where  $V = S \cdot \ell$  is the core volume and  $f$  the frequency.

$P_h$  is proportional to the area enclosed by the  $B$ - $H$  curve and the frequency

$$P_h = K_h \cdot f \cdot B_m^2 \quad (5)$$

where  $K_h$  is the hysteresis loss coefficient and  $B_m$  the flux density amplitude.

According to Faraday's law and Lenz's law, a varying magnetic field causes eddy current to flow in a closed loop, perpendicular to the magnetic field.

Eddy currents in the magnetic core can be expressed in terms of the core material  $K_e$ , the frequency and the peak value of the magnetic field as follows

$$P_e = K_e \cdot f^2 \cdot B_m^2 \quad (6)$$

#### B. Losses in gapped cores

Power loss in a gapped core is lower than that in the ungapped core with the same magnetic flux density  $B_m$ . In that case, the core material power loss density ( $mW/cm^3$ ) due to both phenomena, hysteresis and eddy currents, can be described by the Steinmetz's empirical equation [14]

$$P_f = k \cdot f^a \cdot B_m^b = k \cdot f^a \cdot \left( \frac{\mu_0 N I}{\ell_g + \frac{\ell}{\mu_r}} \right)^b \quad (7)$$

where  $I$  (A) is the amplitude of the sinusoidal inductor current,  $\ell_g$  (mm) the gap length,  $\mu_r$  the relative permeability,  $\mu_0$  the permittivity of free space,  $k$ ,  $a$ , and  $b$  are constants for a given core material at the frequency  $f$  (kHz) of the sinusoidal waveform.

However, given that the inductor is used to filter harmonics, the core material power loss density can be estimated by using Fourier expansion of the inductor current

$$P_c = \sum_{j=1}^{\infty} k_j \cdot (f_j)^{a_j} \cdot \left( \frac{\mu_0 N I_j}{\ell_g + \frac{\ell}{\mu_r}} \right)^{b_j} \quad (8)$$

In the case of the considered amorphous core,  $\mu_r$  remains constant up to 300 kHz. However, at higher frequencies, there is a dependency of  $\mu_r$  on frequency. Thereby, at high frequency  $\mu_r$  becomes complex as is shown in Fig. 3.

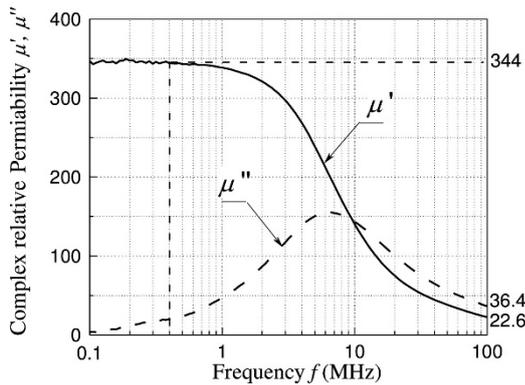


Fig. 3. Complex relative permeability vs frequency of the amorphous core [12].

Moreover, the relationship between the flux density,  $B$  and the magnetic field strength,  $H$  is given by the permeability which depends on  $B$ .

$$B = \mu(B) \cdot H \quad (9)$$

Therefore, the inductance of an inductor depends on the current through the coil or the magnetic flux through the coil.

### C. Inductance characterization

The inductance of the designed inductor has been experimentally characterized versus frequency (Fig. 4) and versus the current through the inductor (Fig. 5).

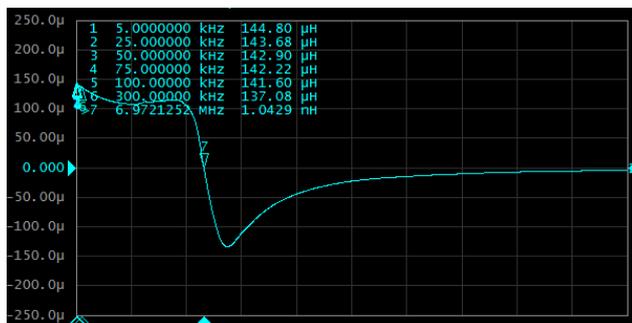


Fig. 4. Measurement with VNA (Vector Network Analyzer) of inductance vs frequency of the Metglas core.

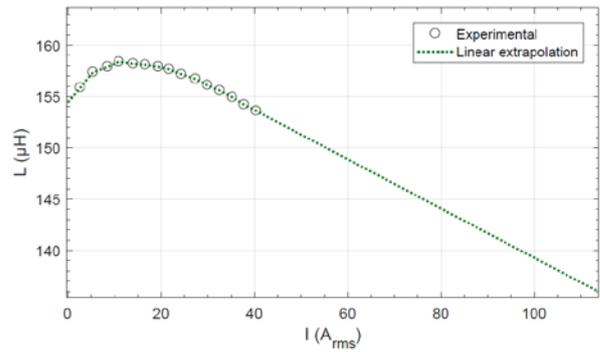


Fig. 5. Measurement of inductance vs current through the inductor.

### D. Experimental set up

The experimental set up consist of a 20-kW SiC inverter (Fig. 6) [15], an LCL filter and a 5.57 kW load. The parallel capacitor has a capacitance of 3  $\mu$ F and a 2.5  $\Omega$  series resistor is added to avoid resonance effects. Finally, a three-phase 1 mH grid-side inductor is used.



Fig. 6. SiC inverter test at 20 kHz and 20 kW.

Three single-phase inductors are used in the inverter side ( $L_1$ ), whose description is given above. To avoid core saturation, a small air gap is placed between two U-cores. Table I shows the key physical characteristics of the designed inductor.

Table I. – Key physical characteristics of the designed inductor ( $L_1$ ).

Turns	18	-
Magnetic path length	0.292	m
Air gap length	0.002	m
Cross-sectional area	0.000545	m <sup>2</sup>
Inductor core mass	1.116	kg

E. Losses in the inductor core

Inverter-side inductor losses have been measured and compared with those calculated using Steinmetz’s equation based on the Fourier expansion of the inductor current.

Theoretical results have been compared with experimental results obtained in inductor  $L_1$  of the SiC inverter shown in Fig. 6.

As it can be noticed in Table II, the experimental values measured in  $L_1$  are compared with the results obtained by using (8), considering different number of harmonics. If only the first carrier and sideband harmonics (first 511 current harmonics) are considered, the error is unacceptable (73.6%). When the 5000 current harmonics are considered, the result become close to the measured value (error = 0.6%). However, the experimental results include all losses and, therefore, calculated losses in the conductor must include Litz wire losses [16], in that case a 6.6 % error is obtained.

Table II. – Comparison between the experimental losses measured in  $L_1$  and the results obtained using (8), considering different number of harmonics.

	LOSSES (W)	ERROR (%)
Using (8) and $I_j$ calculated by simulation with 511 first current harmonics (25.55 kHz)	17.2	73.6%
Using (8) and $I_j$ calculated by simulation with 5000 current harmonics (250 kHz)	65.6	0.6%
Using (8) and $I_j$ calculated by simulation with 5000 current harmonics and including Litz wire losses	69.5	6.6%
Experimental value	65.2	-

From the point of view of the inductor design, it may be more helpful to quantify the error when core losses are obtained from current harmonics calculated by simulation. In this way, and under different assumptions, Table III compares the experimentally measured losses with those values calculated using (8) from simulated harmonics. As it can be noticed, the estimated losses are higher (40% error) than the measured value. On the one hand, the simulated (THD = 55.71% before  $L_1$ ) and experimental (THD = 47.8% before  $L_1$ ) harmonic content are not the same. On the other hand, in Simulink simulations the resistance of the inductor is considered constant, while there is a strong frequency dependence, as shown in Fig.7.

Table III. – Comparison of experimental losses to those calculated by using (8) from current harmonics obtained through simulation.

	LOSSES (W)	ERROR (%)
Using (8) and $I_j$ calculated by simulation with 511 first current harmonics (25.55 kHz)	22.1	66%
Using (8) and $I_j$ calculated by simulation with 5000 current harmonics (250 kHz)	87.2	34%
Using (8) and $I_j$ calculated by simulation with 5000 current harmonics and including Litz wire losses	91.1	40%
Experimental value	65.2	-

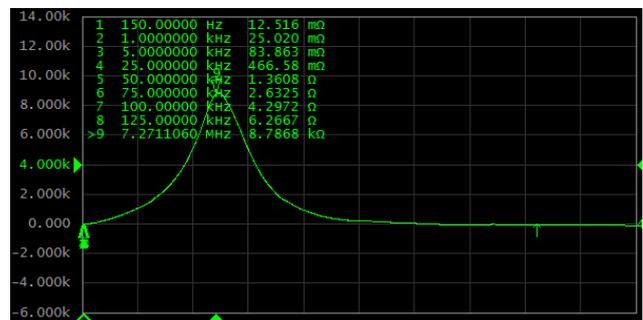


Fig. 7. Frequency dependence of the resistance of  $L_1$ .

In addition, the SiC inverter characterization at this load level gives 66.9 W of losses. The power dissipated in the inverter-side inductor ( $L_1$ ) (65.2 W) is mainly due to high frequencies, since losses at 50 Hz are only 0.9 W. Fig. 8 shows the measured voltages at both sides of  $L_1$ .

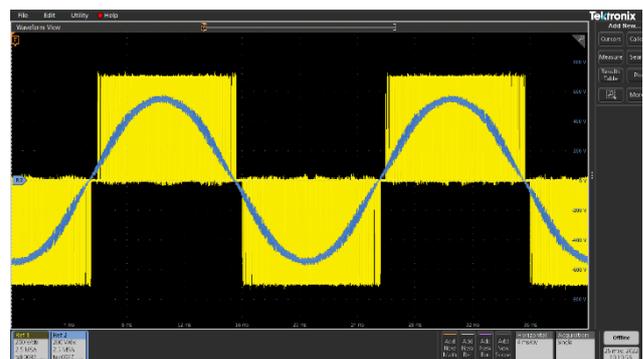


Fig. 8. Experimental voltages in  $L_1$  (voltage in the inverter-side in yellow, voltage in the grid-side in blue).

In the same way, measured losses in the RC branch ( $R = 2.5 \Omega$  and  $C = 3 \mu F$ ) are 108 W; however only 0.34 W are the losses at 50 Hz. Finally, in the grid-side inductor losses are equal to 51 W.

## Conclusions

Losses have been measured in the components of a 20-kW SiC inverter. Results confirm that the inverter-side inductor of the LCL filter, together with the RC parallel branch, are essential to suppress higher frequency harmonic content.

A good estimation of core losses in the inverter-side inductor is a relevant parameter in the LCL filter design. Core losses are calculated using (8), Steinmetz's equation based on the Fourier expansion of the inductor current. Results (Table II) obtained using (8) considering different number of harmonics have been compared to the measured core losses. When all carrier and sideband harmonics (5000 current harmonics) are considered, the error is equal to 6.6%.

To check the core losses in the inverter-side inductor in the LCL filter design, experimental losses have been compared (Table III) with those values calculated using (8) from simulated harmonics. In this case, the estimated losses are higher than the measured values, with an error of about 40%, which is totally acceptable.

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