

29635 - Fundamentals of Electrical Engineering

Información del Plan Docente

Academic Year	2016/17
Academic center	110 - Escuela de Ingeniería y Arquitectura
Degree	430 - Bachelor's Degree in Electrical Engineering
ECTS	6.0
Course	1
Period	Second semester
Subject Type	Compulsory
Module	---

1.Basic info

1.1.Recommendations to take this course

1.2.Activities and key dates for the course

2.Initiation

2.1.Learning outcomes that define the subject

2.2.Introduction

3.Context and competences

3.1.Goals

3.2.Context and meaning of the subject in the degree

3.3.Competences

3.4.Importance of learning outcomes

4.Evaluation

5.Activities and resources

5.1.General methodological presentation

The learning process is based on:

The learning process is developed in three main levels: theoretical lectures, practical lectures and lab practices. Theoretical lectures are devoted to present the basis of Circuit Analysis, showing examples. Practical lectures are dedicated to show type problems, so the student can analyze similar situations. The lab practices are taken place in small groups, so the student can implement the knowledge acquired in lectures.

5.2.Learning activities

29635 - Fundamentals of Electrical Engineering

The course offered to the student includes these activities:

Theoretical lectures (30 presential hours)

Basis and concepts of electrical circuit analysis are presented, showing real examples. The student participation is encourage through small questions and discussions. The contents to develop are detailed in the program.

Practical lectures(15 presential hours)

Type problems are presented, promoting the student participation, and coordinated with the theoretical lectures. Some of these lectures can be intended for the assessment of learning activities.

Lab practices (15 presential hours)

The student estimates, simulates, mounts and checks the operation of electrical circuits in the laboratory. The student has to prepare previously the script practice. Some of these hours can be intended for the assessment of learning activities.

Other evaluable activities

These activities consist of personal student work as well as presential hours (included in previous sections).

Assessment (3 presential hours)

Its main purpose is to obtain a mark, but it is also a learning tool, so the student can check his comprehension and assimilation level.

Tutorials

They are devoted to direct care to the student, to identify learning problems, to guide throughout the subject, to attend and solve exercises and assignments...

Supervised works (18 non presential hours).

The student has to analyze exercises and problems on his own or in groups. These hours include the preparation of lab practices and additional activities.

29635 - Fundamentals of Electrical Engineering

Personal study (70 non presential hours).

Continuous study is encouraged by a uniform distribution of learning activities throughout the semester.

5.3.Program

CHAPTER 1: FUNDAMENTAL QUANTITIES AND CIRCUIT ELEMENTS

CHAPTER 2: CIRCUIT ANALYSIS METHODS. APPLICATION IN RESISTIVE NETWORKS

CHAPTER 3: STEADY STATE IN AC CIRCUITS

CHAPTER 4: MAGNETIC COUPLING BETWEEN COILS

CHAPTER 5: POWER IN AC CIRCUITS

5.4.Planning and scheduling

Scheduling of presential lectures and presentation of assignments

Theoretical and practical lectures, as well as laboratory practices, are held according to the timetable set by the Faculty, and they are published before the start date of the course (<http://eina.unizar.es>).

The lecturer will report about his tutorial timetable.

Other activities will be planned according to the number of students and they will be announced with sufficient time. They will be available in <http://moodle.unizar.es>

5.5.Bibliography and recomended resources

Bibliography can be found in <http://psfunizar7.unizar.es/br13/eGrados.php?id=220>