

30332 - Telecommunications Electronic Systems

Información del Plan Docente

Academic Year	2016/17
Academic center	110 - Escuela de Ingeniería y Arquitectura
Degree	438 - Bachelor's Degree in Telecommunications Technology and Services Engineering
ECTS	6.0
Course	4
Period	First semester
Subject Type	Compulsory
Module	---

1.Basic info

1.1.Recommendations to take this course

1.2.Activities and key dates for the course

2.Initiation

2.1.Learning outcomes that define the subject

2.2.Introduction

3.Context and competences

3.1.Goals

3.2.Context and meaning of the subject in the degree

3.3.Competences

3.4.Importance of learning outcomes

4.Evaluation

5.Activities and resources

5.1.General methodological presentation

The learning process that is designed for this subject is based on the following:

A learning methodology based on projects carried out by groups of students. The process is guided throughout its development and throughout the different methodologies used.

Theoretical presentations (M1) are included to add contents on components, circuits and design methods, specific key points will be presented and developed in seminar sessions (M2).

Learning on computer simulation tools and design tools, as well as on electronic assembly and debugging, is formalized in practice sessions (M8, M9). Some of these practical sessions are presented as stand-alone problems whose solution will be reused in the project (M5).

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Other non-contact type methods work and theoretical and practical studies (M12, M14) are also used. The background work of students is the development of a guided project, through which they will acquire the desired skills with teacher supervision (M6). individual tutorials are planned and work in group are scheduled to monitor the development of projects (M10). Evaluation is also a time of learning and one of its main moments is the presentation of the project by the group (M7, M11).

5.2.Learning activities

Because of the methodology to be applied, the program takes the form of a schedule of activities to be developed while the groups advance in the design. The theoretical aspects will be introduced as needed for the development of design.

5.3.Program

1. Statement. Presentation of the methodology. Examples.
2. Specifications, planning and objectives.
3. Preliminary design: Block Diagram .. Selecting technologies.
4. Preliminary design: Legal Considerations (regulations). mechanical considerations. Other considerations (ergonomic, thermal, etc.).
5. Preliminary design: Channel Analysis and implications for electronic design.
6. Electronic design of the transmitter. Component selection. Construction of the prototype.
7. Electronic design of the receiver. Component selection. Construction of the prototype.
8. Installation and set-up the communications link. Assessment of compliance with specifications.
9. Data sheet of the system.
10. Final presentation of the system.

5.4.Planning and scheduling

Given the nature of the subject all the sessions will take place in an electronics laboratory where the students have to develop all planned activities. The calendar and schedule of these sessions will adhere to the directive of the EINA, the presentation of the projects will adhere to the official exam period.

5.5.Bibliography and recommended resources