

30028 - Power Electronics

Información del Plan Docente

Academic Year	2017/18
Faculty / School	110 - Escuela de Ingeniería y Arquitectura
Degree	436 - Bachelor's Degree in Industrial Engineering Technology
ECTS	6.0
Year	3
Semester	Second semester
Subject Type	Compulsory
Module	---

1.General information

1.1.Introduction

1.2.Recommendations to take this course

1.3.Context and importance of this course in the degree

1.4.Activities and key dates

2.Learning goals

2.1.Learning goals

2.2.Importance of learning goals

3.Aims of the course and competences

3.1.Aims of the course

3.2.Competences

4.Assessment (1st and 2nd call)

4.1.Assessment tasks (description of tasks, marking system and assessment criteria)

Course grading

The course is assessed according to the following items:

Final exam mark CT, from 0 to 10 points, has a weight of 75% in the final mark.

Laboratory classes mark CL, from 0 to 10 points, has a weight of 25% in the final mark.

The final mark (CG) is obtained using the following equations:

$$CG_{aux} = 0.25 \times CL + 0.75 \times CT$$

$$CG = CG_{aux} \text{ if } (CL \geq 4 \text{ and } CT \geq 4), \text{ otherwise } CG = \min\{4, CG_{aux}\}$$

You need a final mark greater than or equal to 5 to pass the course.

5.Methodology, learning tasks, syllabus and resources

5.1.Methodological overview

The methodology followed in this course is oriented towards achievement of the learning objectives. It is based on participation and the active role of the student favors the development of communication and decision-making skills. A wide range of teaching and learning tasks are implemented, such as lectures, guided assignments, laboratory sessions, autonomous work, and tutorials.

Students are expected to participate actively in the class throughout the semester.

Classroom materials will be available via Moodle. These include a repository of the lecture notes used in class, the course syllabus, as well as other course-specific learning materials.

Further information regarding the course will be provided on the first day of class.

5.2.Learning tasks

The course includes 6 ECTS organized according to:

- Lectures (1.8 ECTS): 45 hours.
- Laboratory sessions (0.6 ECTS): 15 hours.
- Guided assignments (1 ECTS): 25 hours.
- Autonomous work (2.4 ECTS): 60 hours.
- Assessment (0.2 ECTS): 5 hours.

Notes:

Lectures : the professor will explain the theoretical contents of the course and solve illustrative applied problems. These problems and exercises can be found in the problem set provided at the beginning of the semester.

Laboratory sessions : sessions will take place every 2 weeks (5 sessions in total) and last 3 hours each. Students will work together in groups actively doing tasks such as simulations, practical demonstrations, measurements, calculations, and the use of graphical and analytical methods.

Guided assignments : students will complete assignments related to concepts seen in laboratory sessions and lectures.

Autonomous work: students are expected to spend about 60 hours to study theory, solve problems, prepare lab sessions, and take exams.

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Tutorials : the professor's office hours will be posted on Moodle and the degree website to assist students with questions and doubts. It is beneficial for the student to come with clear and specific questions.

5.3.Syllabus

The course will address the following topics:

Theory sessions

Topic 1. Fundamentals of Microcontrollers (6 hours)

Topic 2. Digital Design with the MSP430 Microcontroller Family (8 hours)

Topic 3. Fundamentals of Power Electronics (5 hours)

Topic 4. 1. DC-DC Power Converters (5 hours)

Topic 5. 2. DC-AC and AC-AC Power Converters (6 hours)

Topic 6. Rectifiers (6 hours)

Topic 7. 4. Power Electronics Technologies (8 hours)

Laboratory sessions

Session 1. Introduction to the Design with Microcontrollers

Session 2. PWM Motor Speed Control with Microcontroller

Session 3. DC-DC Converters: Simulation and Hands-On

Session 4. Power Inverters Simulation and Demo

Session 5. Lamp Dimmer with a Microcontroller Driven Thyristor

5.4.Course planning and calendar

For further details concerning the timetable, classroom and further information regarding this course, please refer to the Escuela de Ingeniería y Arquitectura de la Universidad de Zaragoza (EINA), website, <https://eina.unizar.es/> .

5.5.Bibliography and recommended resources

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Bibliography: [**BB** -Basic Bibliography / **BC** -Additional Bibliography]

- BB Hart, Daniel W.. Electrónica de potencia / Daniel W. Hart ; traducción, Vuelapluma ; revisión técnica, Andrés Barrado Bautista...[et al.] . - 1a. ed. en español Madrid [etc.] : Prentice-Hall, D.L. 2001
- BC Davies, John H.. MSP430 microcontroller basics / John H. Davies Burlington, MA [etc] : Newnes, cop. 2008
- BC Martín del Brío, Bonifacio. Sistemas electrónicos basados en microprocesadores y microcontroladores / Bonifacio Martín del Brío . - 1ª ed. Zaragoza : Prensas Universitarias de Zaragoza, 1999
- BC Mohan, Ned. Power electronics : converters, applications and design / Ned Mohan, Tore M. Undeland, William P. Robbins . - 3rd. ed. [New York] : John Wiley & Sons, cop. 2003
- BC Problemas de electrónica de potencia / coordinación y revisión técnica Andrés Barrado Bautista, Antonio Lázaro Blanco Madrid [etc.] : Pearson Educación, D.L. 2007

Electronic resources

- Lecture slides, problem set, and instructions for the laboratory sessions are available online in <https://moodle2.unizar.es>
- Texas Instruments, <http://www.ti.com/msp430>.

The students could find resources in the library, for example clicking on <http://biblioteca.unizar.es/como-encontrar/bibliografia-recomendada>