Universidad Zaragoza
1542

Year : 2018/19

30329 - Digital Electronic Systems

Syllabus Information

Academic Year:	2018/19
Subject:	30329 - Digital Electronic Systems
Faculty / School:	110 -
Degree:	438 - Bachelor's Degree in Telecomunications Technology and Services Engineering
ECTS:	6.0
Year:	
Semester:	Second semester
Subject Type:	
Module:	---

General information

Aims of the course

Context and importance of this course in the degree

Recommendations to take this course

Learning goals

Competences

Learning goals

Importance of learning goals

Assessment (1st and 2nd call)

Assessment tasks (description of tasks, marking system and assessment criteria)

Methodology, learning tasks, syllabus and resources

Methodological overview

This course covers the systematic design of advanced digital systems using Field programmable gate arrays (FPGAs) and an introduction to ASIC design.

We will first review in detail the basic building blocks of FPGA programming. Second, we focus on architecture, design methodologies, best design practices, and optimization techniques for performance (frequency, latency, area, power, etc). Finally, we will cover testbench development, simulation for bit-true design verification, and synthesis of complete digital systems.

The emphasis is on FPGA technology, but most of the design techniques can also be applied to ASIC devices.

Learning tasks

The course includes the following learning tasks:

- **Lectures** (45 hours). Students are expected to attend all lectures, pay attention and participate in class discussions.
- **Lab sessions** (15 hours). The course will include 12 lab sessions that allow students to design, implement, test, and evaluate several small communication blocks. Students are expected to work in pairs. It is suggested that students form a group at the beginning of the course and keep in the same group throughout the semester.

Syllabus

course topics:

- Advanced VHDL coding

- Fixed point VHDL description.

- FPGA architectures

- High performance FPGA design

- CMOS Technology

- Introduction to ASIC design

- Testbench development

Course planning and calendar

Further information concerning the timetable, classroom, office hours, assessment dates and other details regarding this course, will be provided on the first day of class or please refer to the EINA website.

Bibliography and recommended resources

recomanded resources:

ISE WebPack **http://www.xilinx.com/support/download/index.htm**